L1: (2) embed$6 near VR
L2: (34136) voltage adj regulator
L4: (415970) embed$5
L5: (1964) 2 and 4
L6: (9779) MCM
L7: (27) 5 and 6
L8: (0) 2 and 3
L3: (68) on adj chip
L9: (5) embedded adj regulator
L10: (2105) 257/678.ccls.
L12: (1976) 257/723.ccls.
L13: (1542) 257/724.ccls.
L11: (14) 2 and 10
L14: (12) 2 and 12
L15: (16) 2 and 13
L16: (2) embedded near VR
L17: (10) embedded same VRM
L18: (0) on adj die adj VR
L19: (0) on adj die
L20: (445) on-die
L21: (1) on-die adj regulator
L22: (4) 2 near 20
L23: (0) 20 near VRM
L24: (1410484) active
L25: (151355) passive
L26: (5127) 10 12 13
L27: (467) 25 and 26
L28: (199) 27 and flip
L29: (118) 28 and die
L30: (1) 29 and 2
L31: (5) "6639309"

Browse   Queue   Clear

DBs: USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

Default operator: OR

Burn

Highlight all hit terms initially

"6639309"

10/8/04

BRS term   S&R bm   Image   Text   HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	2	3		#	Im
1	☐	☐	US 20040036155 A1	20040226	10	Memory package	257/678			Wallace, Robert F.	☑	☐	☐	☐	☐	☐	☐	US
2	☑	☐	US 6797538 B2	20040928	9	Memory package	438/109	438/110		Wallace, Robert F.	☐	☐	☐	☐	☐	☐	☐	US
3	☐	☐	US 6639309 B2	20031028	10	Memory package with a controller on one side of a printed circuit board an	257/678	257/679; 257/723;		Wallace, Robert F.	☑	☐	☐	☐	☐	☐	☐	US
4	☑	☐	US 20040036155 A	20040226		Memory package for complex embedded system e.g. cellular phone,				WALLACE, R F	☐	☐	☐	☐	☐	☐	☐	
5	☑	☐	US 20030183914 A	20031002		Memory package e.g. for flash electrically erasable programmable R				WALLACE, R F	☐	☐	☐	☐	☐	☐	☐	

Hit   Details   HTML

Ready   NUM